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APRIL/MAY 1978

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MICRO is published bi-monthly by The COMPUTERIST, 8 Fourth Lane, So. Chelmsford, MA 01824. Robert M. Tripp, Editor/Publisher. Controlled circulation postage paid at Chelmsford, Massachusetts.

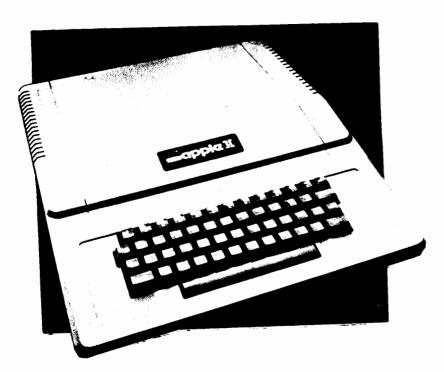
Single Copy: \$1.50 Annual Subscription: \$6.00 (6 issues) in USA.

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The feature article in this issue is "A Complete Morse Code Send/Receive Program for the KIM-1" by Marvin L. De Jong [page 7]. Marvin has had two excellent articles in previous issues of MICRO [Digital-Analog and Analog-Digital Conversion Using the KIM-1, MICRO #2, and, Employing the KIM-1 Microcomputer as a Timer and Data Logging Module, MICRO #3]. His new article, which includes eight pages of source listings should be of interest to all 6502 programmers, even those with zero interest There are a number of in ham radio. useful techniques in the program:

a bit pattern conversion; a table lookup; some interrupt handling; use of the KIM timer

just to mention a few. The ham radio enthusiast will, of course, find a lot of other good stuff, and will probably want to try it with their own equipment.

"The Apple II Chart" [page 4] was submitted by another MICRO regular, C. R. (Chuck) Carpenter. Chuck recommends that the chart be used to layout and keep track of strings for Applesoft BASIC. He suggests making two copies of the page, one for alphabetic and one for numeric variables, placing them between two sheets of plastic, and writing on the plastic with a felt tip pen so that the setup can be erased and used over again.

Cnuck has also written the "Apple II Printing Update" [page 27] as a follow on to his article on "Printing with the Apple II", MICRO #3. Here he presents solutions to a couple of problems he encountered, plus a short note on how to let BASIC do hex-to-decimal conversions for you.

Charles Floto, with a little help from his friends, continues to provide info "The PET about the PET. Vet Examines some BASIC Idiosyncrasies" [page 5] has a discussion of some of the features of a Mailing List Program which was written by Richard Rosner. Charles also some "Early PET-compatible discusses Products" [page 22]. Roy O'Brien assembled a short list of "PET Software from Commodore" [page 21] which covers software and documentation which you may be able to get directly from Commodore if you ask for it nicely.

The extensive "6502 Bibliography" being compiled by William Dial, is continued. Part I [MICRO #1] covered references 1 through 128; Part II [MICRO #3] covered 129 through 179; and Part III continues through reference 300. Suddenly there seems to be a lot of material being written on the 6502. It looks like the secret of what a great little processor it is has gotten "out of the bag". you know of any source of regular info on 6502s that Bill is not covering, how about letting him know about it and perhaps he can get on the subscription or distribution list and include the material in future "6502 Bibliography" parts.

since a "beeper" for the PET is mentioned in one of this issues articles, and since the Apple II already has a built in beeper, it only seemed fair to give the KIM-1 a voice too. Gerald C. Jenkins presents "A Kim Beeper" [page 43] that is easy to build and provides the software to run it.

"The MICRO Software Catalog" [page 23], begins in this issue, and will probably become a regular department. A number of items were received too late for inclusion in this issue, and will be held over for the next issue. Certain items were considered to be too small or of limited interest to be included. We will return these to the senders so that they will know the status of their submission.

While MICRO likes to "accentuate the positive", we would be remiss if we would totally "eliminate the negative". A potentially serious problem with the Apple II has been raised, and a brief discussion is presented in "A Worm in the Apple" [page 32]. We will follow up on this item and present more info next issue.

Rick Auricchio presents "An Apple II Programmer's Guide" [page 45] which contains a lot of information he has discovered which the manual did not cover. Included in the article are a pair of tables which Apple programmers will find useful.

Z ZO ZI ZZ Z3 Z4 Z5 Z6 Z7 Z8 Z9 ZA ZB ZC ZD ZE ZF ZG ZH ZI ZJ ZK ZL ZH ZN ZO ZP ZG ZR ZS ZT ZU ZV ZW ZX ZY ZZ

X XO X1 X2 X3 X4 X5 X6 X7 X8 X9 X4 XB XC XD XE XF XG XH XI XJ XK XL XH XN XD XP XG XR XS XT XU XU XW XX XY XZ Y YO YI Y2 Y3 Y4 Y5 Y6 Y7 Y8 Y9 YA Y8 YC YD YE YF YG YH YI YJ YK YL YM YN YD YP YB YR YS YT YU YU YW YX YY YZ

THE PET VET EXAMINES SOME BASIC IDIOSYNCRASIES

Charles Floto 325 Pennsylvania Ave., S.E. Washington, DC 20003

Richard Rosner has supplied a program listing produced using his RS-232 printer interface for the PET. As it's well commented I'll only point out examples of some of the unusual features of PET BASIC.

Line 1 is an example of the OPEN statement. The first number specifies that it applies to logical file number 5. This is the name by means of which other statements in the program will use this data file. The second number specifies that physical device number 5 is being used. Which device is number 5 is determined by the wiring of the system.

The PET, as sold, is wired for device 0 the keyboard; 1, the built-in tape drive; 2, the auxiliary drive connector on the back; and 3, the screen. Referring to a physical device that hasn't been electrically connected will result in a DEVICE NOT PRESENT ERROR. Richard's system does contain a physical device 5: his RS-232 output port.

If the third number in the OPEN statement is 0, reading the file is enabled. Writing is prepared for by 1, while a 2 here enables file writing with an end-of-tape character to be added when the file is CLOSEd.

Line 2 illustrates the use of CMD. It allows program commands to be applied to a device specified by the logical file connected with it (not by the physical device number). Note that RUN will merely cause a listing to be produced. RUN 5 calls the rest of the program into action.

Line 2000 demonstrates use of the OPEN statement with a variable. Lines 2000-2300 print data either on the tape drive or on the screen depending on which device number is the current value of variable D. In each case logical file 8 is used.

Another idiosyncrasy comes up here: while PRINT may be entered as ?, PRINT# cannot be entered as ?# - it must be spelled out. Otherwise a SYNTAX ERROR will result when the program is run, even though the listing will look alright.

But you can still save a good deal of typing entering these lines. Once 2110 is in simply move the cursor up to change the line number to 2111 and NA to AD. Then hit RETURN and you'll have both 2110 and 2111 in memory.

I suggest you make a few changes in Richard's program. Add 105 DIM ST\$(CO) Consider storing the zip code as a string rather than as an integer. Repeat lines 2000-2300 as 5000-5300 (by changing the first digit in each line number) and change line 4500 accordingly. Then you can alter the display format without messing up the tape format. And remember that you can slow screen printing by holding the RVS key down.

A final note: I understand Commodore is now using a different tape drive and recording system. This may create compatibility problems in exchanging programs between the early PETs and the later ones.

- 1 ()PEN 5,5.1,"Mailing List Program (Incomplete)"
- 2 CMD5:PRINT"":LIST: ÉND
- 5 REM THE ABOVE LINES LIST THE PROGRAM ON THE HARD COPY UNIT 10 REM
- 11 REM WRITTEN BY RICHARD ROSNER
- 12 REM BROOKFIELD. CONN.
- 13 REM FOR THE COMMODORE PET.
- 14 REM PRINTED ON A GE PRINTER
- 15 REM USING A PET ADA AVAILABLE FROM THE AUTHOR.
- 49 REM D=DEVICE CODE

```
50 D=1:REM TAPE DRIVE #1
 70 C()=50
 →1 REM CO=MAX NO. OF RECORDS IN LIST
 100 DIM NA$(CO).AD$(CO).CI$(CO)
 101 REM NAS=NAME, ADS=ADDRESS, CIS=CITY
 102 REM ST$=STATE, Z=ZIP CODE
 103 REM KC=KEY CODE. UP TO 10 FOR EACH ADDRESS
 110 DIM Z(CO), KC%(10,CO)
 997 REM ENTER RECORDS FOR MAILING LIST
 998 REM EXIT ON '!' FOR NAME
 1000 FOR N=0 TO CO
 1010 INPUT"NAME":NA$(N)
 1020 IF NA$(N)="!" GOTO 2000
 1025 LN=N
 1030 INPUT"ADDRESS"; AD$(N)
 1040 INPUT"CITY, STATE"; CI $(N), ST$(N)
 1050 INPUT"ZIP CODE"; Z(N)
 1060 FOR NI=0 TO 10
 1070 PRINT "KEY#";N1;:INPUT KC%(N1,N)
 1080 IF KC%(NI,N)=0 GOTO 1180
 1100 NEXTNI
 1180 NEXT N
 1998 PRINT ON TAPE DRIVE(D=1) OR SCREEN (D=3)
 2000 OPEN 8.D.I."ADDRESS FILE"
 2009 REM LN=NUMBER OF RECORDS
 2010 PRINT#8,LN
 2100 FOR N=0 TO LN
 2110 PRINT#8, NA$(N)
 2111 PRINT#8, AD$(N)
 2112 PRINT#8,CI$(N)
 2113 PRINT#8,ST$(N)
 2115 PRINT#8.Z(N)
 2120 FOR NI=0 TO 10
 2130 PRINT#8, KC%(NI, N)
 2150 NEXT NI
 2200 NEXT N
 2300 CLOSE 8
 3000 END
 3997 REM ENTER AT 4000 TO READ IN FROM TAPE
 3998 REM DRIVE NO. I AND THEN PRINT ON SCREEN
 4000 OPEN 8.1.0."ADDRESS FILE"
 4010 INPUT#8.LN
 4011 PRINTLN: KEM PRINT RECORD COUNT
 4100 FOR N=0 TO LN
 4110 INPUT#8, NA $(N)
 4120 REM IF STI AND 64 GOTO 4300
 4130 INPUT#8, AD$(N)
 4131 INPUT#8,CI$(N)
 4132 INPUT#8,ST$(N)
 4135 INPUT#8, Z(N)
 4140 FOR NI=0 TO 10
 4150 INPUT#8,KC%(N1,N)
 4160 NEXTNI
 4190 PRINTN: REM PRINT RECORD NO. AS READ
 4200 NEXT N
 4300 CLOSE 8
4500 D=3:GOTO 2000
READY.
```

A COMPLETE MORSE CODE SEND/RECEIVE PROGRAM FOR THE KIM-1

Marvin L. De Jong, KOEI Dept. of Math-Physics The School of the Ozarks Point Lookout, MO 65726

INTRODUCTION

The program described below will convert ASCII from a keyboard to a Morse code digital signal which can be used to key a transmitter. It will also convert a Morse code digital signal to ASCII for display on the user's video system. Suitable references for circuits to convert the audio signal from a communications receiver to a digital Morse signal are also given. [1,2]

The entire program resides in the memory on the KIM-1, and has the following features:

- 1. The precise code speed in words per minute can be entered at any time from the keyboard. Key in CONTROL S followed by any two-digit decimal number from 05 to 99 words per minute.
- 2. The operator can type as many as 256 characters ahead of the character currently being sent. One page of memory is devoted to a FIFO buffer.
- 3. When there are less than 16 characters left in the buffer, the KIM-1 display indicates how many characters are left (F to 0 hex).
- 4. Backspace capability is provided. CONTROL B erases the last character entered into the buffer, and the operator then enters the correct character.
- 5. The buffer can be pre-loaded with as many characters (up to 256) as desired while the program is in the receive mode. Pressing CONTROL G starts the program sending code as soon as the operator is ready.
- 6. CONTROL R sends the program from the send mode to the receive mode.
- 7. While in the receive mode the display on the KIM-1 informs the operator to either increase the code speed (F, for faster, on the display) or decrease (S, for slower) the speed for proper reception. The receive program actually tolerates a large range in code speeds with no adjustment.

- 8. The feature just mentioned can be used to measure the "other guy's" code speed.
- 9. If the receive mode is not used, any CONTROL key not mentioned above will put the program in an idle loop so the buffer can be loaded. CONTROL G starts the message.
- 10. The carriage return key restarts the send program, or it can be returned from the receive mode to the send mode with CONTROL G.

The KIM-1 was first programmed to send code by Pollock [3], and some of the features of his program are found here. Pollock [4] has also described a microprocessor controlled keyboard using the 6504. It has more features than his original program written for the KIM-1, but the program described here has some additional features which are very attractive, especially the receive program.

II. BACKGROUND

A. Sending Morse Code (ASCII to Morse)

A negative going 10 microsecond strobe pulse from the keyboard is connected to the NMI pin on the KIM-1. Whenever a key is pressed an NMI interrupt occurs and the ASCII code from the keyboard is read at the lowest 7 pins of port A (PAD). The eighth bit is held high, so the number read is actually the ASCII code plus 80 hex. This number is stored in the FIFO buffer which is page 2 of memory on the KIM-1. The send routine uses the numbers in the FIFO memory to index a location in page zero which contains the information to construct the Morse character.

An illustration will make this clear. The ASCII hex representation of the letter C is 43. The strobe pulse causes port A to be read, which results in the number C3 (C3 = 43 + 80) being stored in the FIFO. When the send routine gets to the location in the FIFO where C3 is stored, it uses it to

locate the contents of address 00C3. In location C3 in zero page is found 1A which is 00011010 in binary. The most significant 1 is simply a bit which indicates that all lesser significant bits contain the code information, namely 1 = dash and 0 = dot. Thus, C is dash-dot-dash-dot (1010).

The program causes the 00011010 to be rotated left (ROL) until a 1 appears in the carry position. The carry flag set causes the program to analyze the remaining bits for their code content. It does this by successively rotating them (ROL) into the carry position. If a 1 appears in the carry position, PBO is held at logical 1 for the appropriate time followed by a space while PBO is at logical 0. If a 0 appears in the carry position a dot is sent, followed by a space. When a total of 8 ROL commands have been completed, counting those needed to find the leading 1, then PBO is held at logical O for an additional time to give a character space. The space bar produces still more time at logical 0 to produce a word space.

CONTROL S changes the NMI interrupt vectors so that the next two characters (hopefully decimal digits) from the keyboard are read, converted from base ten to hex [5], and converted to the basic time unit (see below). The interrupt vectors are then restored so that further characters from the keyboard are read as usual. Control characters are obtained by pressing the control key followed by the appropriate control character.

B. Timing Considerations.

Before going much further, the timing calculations will be described. Morse code is a variable length code. That is, the number of bits is variable as contrasted to a fixed bit-length code such as ASCII. Its structure is based on the time duration of the various components as follows:

Mark Elements:

Dot = 1t Dash = 3t

Space Elements

Element space = 1t
 (time between dots and dashes)
Character space = 3t
 (time between letters)
Word space = 7t
 (time between words)

The time t depends on the code speed. According to The Radio Amateur's Handbook a code speed of 24 words per minute (wpm) corresponds to 10 dots per second. Since there are 10 element spaces included in the 10 dots per second, there are a total of 20 t in one second: that is, t=1/20 second at 24 wpm. At any other speed then

t = (1/20)(24/S)= (50 ms)(24/S) = (1200/S) in milliseconds (ms)

where S is the code speed in wpm. If the divide-by-1024 timer on the KIM is used, 1 count corresponds to 1.024 ms. The number T (called TIME in the program) to be loaded into the timer is then

T = (1172/S) base ten or = (494/S) hex.

The speed S in wpm is entered in decimal from the keyboard, converted to base 16 (hex), sent to a divide routine to find T, and T is stored at 0000 in memory. 99 wpm gives 0C hex in TIME while 05 wpm gives EB hex. Care was taken in developing the above calculations because of a discrepancy between it and the results given by Pollock[4].

The system timing was tested by comparing it with code sent by W1AW. The speeds are the same to better than one word per minute from 5 wpm to 35 wpm.

In the receiving program a word space is detected when a space counter exceeds 5T. At moderate code speeds 5T is greater than 255 resulting in an overflow. Consequently, in the receive program 1/2T is used as the basic time unit. In this case, speeds as low as 12 wpm can be received. At slower speeds the system still works, but word spaces occur between each letter.

C. Receiving Morse Code (Morse to ASCII)

To receive Morse code and convert it to ASCII, the inverse of the above process is carried out. It is assumed that a suitable audio detection circuit [1,2[produces a logical 1 for a space element and a logical O for a mark element. This digital Morse signal is applied to PB7 and the IRQ pin on the A character register begins KIM-1. with a 1 in the zero bit position. Each time a dot is received the character register is shifted left and a zero is loaded into the character register. Each time a dash is received the character register is shifted left and a one is loaded into the zero bit position. Thus, when a character space is detected, and a C (for example) has been received, the character register will contain 1A, just as in sending a However, the 1A is used to index a zero page location which contains the ASCII code for C, namely 43. The various components are identified by timing their duration.

III. THE PROGRAMS

A detailed listing of the programs is given below. The detailed comments should allow the reader to understand, modify, and trouble-shoot the program.

A. The Send Program

Some important variables, their meanings, and their locations in zero page are given:

Name Location Use

TIME 0000 TIME is the quantity T mentioned in the section on timing considerations. It is the time, in units of 1.024 ms, of the dot or element space components.

SPEED 0013 SPEED is the hex equivalent of the number entered for the speed by the operator.

PNTR 0015 PNTR is a number which points to the location in the FIFO memory which contains the character currently being sent. The program idles as long as Y = PNTR, but begins to send when Y exceeds PNTR.

Name Location Use

LO 001E Scratchpad location for division of 494 by SPEED to give TIME.

HI 001F Same use as LO.

CNTR 0022 CNTR keeps track of how many characters are left in the FIFO memory. A character entered decrements CNTR; a character sent increments CNTR.

CHEK 0024 Scratchpad location to count the number of numbers which have been entered after the control S has been entered.

YREG 00F4 The Y register is used to point to the location in the FIFO memory where the last character entered from the keyboard is, namely 0200, Y.

B. The Receive Program

Some important variables, their meanings, and their locations are given:

Name Location Use

XREG 00F5 The X register is the character register. It begins with a 1 in the 0-bit. It is shifted left for each mark element received and loaded with a 1 for a dash and a zero for a dot. Later it is used to index a table in zero page which has the ASCII code for the character.

MCNTZ 0054 If a mark element (dot or dash) is being received (PB7 and IRQ at logical 0) the mark counter is incremented at a rate of 1 count every 2.048 ms.

SCNTZ 00EE Same as mark counter except the incrementing occurs when a space is being detected (PB7 high and IRQ high). Rate is also 1 count every 2.048 ms.

HALFT 0051 If the SPEED is set correctly, the number of counts during a dot should be exactly 1/2 TIME. This is the "dot length". If MCNTZ exceeds 1/2 the dot length the program decides that a valid mark character has been received. HALFT is 1/2 the dot length. A valid space element occurs when SCNTZ exceeds HALFT.

Name Location Use

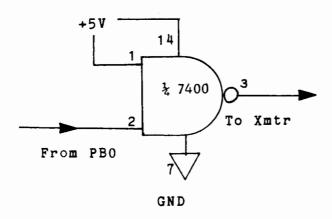
TWOT 0052 TWOT is twice the dot length and is used to decide if a dot or a dash has been received. If MCNTZ exceeds TWOT the element is a dash, otherwise it is a dot.

FIVET 0053 FIVET is five times the dot length and is used to decide when a word space has been received.

IV. INTERFACE

The keyboard strobe is connected to the NMI pin on the expansion connector on the KIM-1, and the 7 bit ASCII code from the keyboard goes to pins PAO-PA6, the low order bit to PAO and the high order bit to PA6. PA7 should be pulled up with a 10K resistor.

The author's transmitter is a solid-state Triton IV and can be keyed with TTL IC's. The circuit diagram below indicates how it was connected to the KIM-1. Transmitters using grid-block keying or cathode keying cannot use this circuit. A relay driven by a Darlington pair connected to pin PBO should work. The KIM-1 manuals give the appropriate details.



The audio from the receiver must produce a logical 0 at pin PB7 and the IRQ pin when a tone is detected, and a logical 1 at the same pins when a space is detected. The reader is urged to try either of the circuits found in references 1 and 2. I used a half-baked scheme in which the audio from the receiver was fed to a half-wave rectifier (diode), filtered slightly, and connected to the inverting input of a CA3140 op amp. The voltage at the non-inverting input was adjustable. The op

amp was operated as an open-loop comparator with the output connected to pin PB7 and IRQ. An oscilloscope was necessary to monitor the output and make the necessary adjustments for various signal levels. I am not recommending this circuit for general use.

I have also tried using the tape-input PLL system on the KIM-1 to convert the receiver audio to a digital signal. lower the free-running frequency of the VCO a shunt capacitor must be added. The digital signal appears at address 1742, bit 7. I had only marginal success, the problem being that the digital signal tends to drop out for very short periods of time, which clears the mark counter (instructions 039F-03A2). Substituting NOP's for these instructions seems to improve the performance, but receiver tuning and volume control adjustments are sensitive. Some users may wish to experiment with deleting the aforementioned instructions in whatever interface circuit they may use.

V. MISCELLANEOUS REMARKS

To get the entire Send/Receive program in the KIM-1 memory extensive use was made of page 1. This is also used as the stack. Care was taken to leave enough room for the stack operations, and for insurance, there are several points in the program where the stack pointer is initialized to FF. No problems should be encountered once the program is up and running. If you have any debugging to do I suggest using the single-step mode (be sure to set the NMI vectors) to check the jumps and branches. My experience has been that errors in branches generally result in about half the program being wiped out, especially if it is in page 1 of memory.

Wouldn't it be nice if some outfit like The COMPUTERIST would offer an interface board which would provide an audio to digital Morse circuit, a relay driver and relay (reed type) for transmit, a DIP socket for a ribbon cable from the keyboard, and a DIP socket for the ASCII out (see appendix), all on a single board which would mate with the KIM-1 application socket.

The first time I operated the system, I answered a CQ on 40 meters from WB2GMN,

Hank, who has Army Signal Corps experience. Even though he rated his speed at 55 wpm he copied me at 60 wpm. Hank reported that the code sounded like perfect code (which it should be) and that it was very crisp at 60 wpm. It was a real coincidence to contact someone who had the capability to appreciate the keyboard system and to give an evaluation of its performance.

I hope that you enjoy working these programs. If you do not want the receive program, simply put in a JMP 0300 instruction (4C 00 03) starting at 0300. If you have any questions, feel free to write, enclosing a SASE for a response. I will try to answer any questions about interfacing the system to your station.

References:

- [1] Steber, G. R., and Reyer, S. E., "The Morse-A-Letter", Popular Electronics, January, 1977.
- [2] Riley, T. P., "A Morse Code to Alphanumeric Converter and Display", in three parts, QST for October, November and December, 1975.
- [3] Pollock, James W., "1000 WPM Morse Code Typer", 73 Magazine, January, 1977.
- [4] Pollock, James, W., "A Micro-processor Controlled CW Keyboard", Ham Radio, January, 1978.
- [5] Ward, Jack, "Manipulating ASCII Data", Kilobaud, February, 1978.

ACSII to MORSE and MORSE to ASCII Lookup Tables in Page Zero

00.	ХX	20	45	54	49	41	4 E	4 D	53	55	52	57	44	4 B	47	4 F
10	48	56	46	ХX	4 C	ХX	50	4 A	42	58	43	59	5 A	51	ХX	ХX
20	35	34	ХX	33	ХX	ХX	ХX	32	ХX	XX	ХX	ХX	ХX	ХX	ХX	31
30	36	3 D	2 F	ХX	ХХ	ХХ	ХХ	ХХ	37	ХХ	ХX	ХХ	38	ХX	39	30
40	ХX	ХX	ХХ	ХX	ХX	ХX	ХХ	ХX	XΧ	XΧ	ХХ	ХХ	3 F	ХХ	XΧ	ХX
50	ХX	ХX	ХX	ΧX	ХX	2 E	ХX	ХX	ХX	XX	$\mathbf{X} \cdot \mathbf{X}$	ХX	ХX	ХX	ХX	ХX
A O	80	ХХ	XΧ	2 A	45	ХX	ХX	XΧ	XΧ	XΧ	ХХ	XΧ	73	ХX	55	32
BO	3 F	2 F	27	23	21	· 20	30	38	3 C	3 E	ХX	ХХ	ХХ	31	ХX	4 C
CO	ХX	05	18	1 A	0 C	02	12	ΟE	10	04	17	0 D	14	07	06	0 F
D0	16	1 D	0 A	08	03	09	11	0 B	19	1 B	1 C	ХX	XΧ	ХX	XΧ	XX

Special Morse Characters

Keyboard Character

BT	=
SK	\$
AR	#
Space (Word)	Space Bai

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e page with any data 4F FB SF FB FS SE 3 90 00 00 8 3 4 EA EA 74 73 AB (This fill EA EA EA E 탪 87 EA 出出 0000 J ØØØE

iow address of destination, C.P. and get offset Supply low address of branch instruction Inspect and Change DC 18 *I 0100 3100 A9

A. 2.4

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AVERSION

TAHW SI

```
TIME
                             $0000
                                            MORSE CODE SEND PROGRAM
               ZTB
                             $0000
               SPEED
                             $0013
               PNTR
                             $0015
               LO
                             $001E
               ΗI
                             $001F
               CNTR
                             $0022
               CHEK
                             $0024
                                    1/2 DOT TIME
                             $0051
               HALFT
                                    TWICE DOT TIME
               TWOT
                             $0052
                                    FIVE TIME DOT TIME
               FIVET
                             $0053
                             $0054
               MCNTZ
                             $00EE
               SCNTZ
               FIFO
                             $0200
                                    AUTHORS DISPLAY DEVICE
                             $13F9
               CULO
                                    REGISTERS
               CUHI
                             $13FA
               DATA
                             $13FB
                                    NON-MASKABLE INTERRUPT LOW
               NMIL
                             $17FA
                                    NON-MASKABLE INTERRUPT HIGH
               NMIH
                             $17FB
                             $17FE
                                    INTERRUPT REQUEST LOW
               IRLO
                                    INTERRUPT REQUEST HIGH
               IRHI
                             $17FF
                             $1700
                                    PORT A DATA
               PAD
                                    PORT A DATA DIRECTION
                             $1701
               PADD
                                    PORT B DATA REGISTER
                             $1702
               PBD
                                    PORT B DATA DIRECTION REGISTER
               PBDD
                             $1703
                                    KIM DISPLAY
                SAD
                             $1740
                             $1741
                                    KIM DISPLAY DIRECTION
                SADD
                             $1742
                SBD
                             $1743
                SBDD
                                    DIVIDE BY 64 TIMER
                TIM
                             $1706
                                     DIVIDE BY 1024 TIMER
                TMER
                             $1707
                             $1FE7
                                     KIM ROM CHARACTER TABLE
                TAB
0056
                       ORG
                             $0056
0056 D8
               INIT
                       CLD
                                    INIT SEQUENCE. CLEAR DECIMAL
0057 A9 FF
                       LDAIM SFF
                       STAZ TIME
                                    INITIAL CODE SPEED OF 18 WPM
0059 85 00
               RTN
                                    PREVENT INTERRUPTS
                       SEI
005B 78
005C A2 FF
                       LDXIM SFF
                                    FROM RECEIVER
005E 9A
                                    SET STACK POINT TO TOP $01FF
                       TXS
005F A9 20
                       LDAIM VCTL
                                    SET NIM VECTORS FOR KEYBOARD
0061 8D FA 17
                       STA
                             NMIL
                      LDAIM VCTL
0064 A9 01
                       STA
                             NMIH
0066 8D FB 17
0069 A9 00
                      LDAIM $00
006B 8D 01 17
                       STA
                             PADD
                                    PORT A IS INPUT PORT
006E 8D 02 17
                       STA
                             PBD
                                    PORT B, PIN PBO, WILL BEGIN AT O
                                    PORT B, PIN PBO, IS OUTPUT PIN
0071 A9 01
                       LDAIM $01
                             PBDD
0073 8D 03 17
                       STA
                                     SET UP DISPLAY PORTS
0076 A9 7F
                       LDAIM $7F
                                     PINS 0 - 6 ARE OUTPUT PINS
0078 8D 41 17
                       STA
                             SADD
                       LDAIM $1E
007B A9 1E
007D 8D 43 17
                                    PINS 1 - 4 ARE OUTPUT PINS
                       STA
                             SBDD
                                    INIT LEFTMOST DIGIT
0080 A9 08
                       LDAIM $08
```

008A AO FF 008C 84 15 008E 84 22 0090 C4 15 0092 FO FC 0094 E6 15 0096 A6 15	LOOP	LDAIM STA LDYIM STYZ STYZ CPYZ BEQ INCZ LDXZ LDXZ LDAX	\$FF PNTR CNTR PNTR LOOP PNTR PNTR FIFO	BLANK DISPLAY BY PUTTING 80 IN PORT SAD INIT Y POINTER INIT SEND POINTER INIT BUFFER COUNTER IS Y = PNTR? YES, IDLE UNTIL DIFFERENT
	DISPLA	Y SUBR	OUTINE	
0100		ORG	\$0100	
0102 E0 10 0104 90 08 0106 A9 80 0108 8D 40 17 010B 4C 14 01		CPXIM BCC LDAIM STA JMP	\$10 OVER \$80 SAD THER	YES, DISPLAY CNTR NO, BLANK DISPLAY
0111 8D 40 17 0114 60	THER	STA		
0115 20 80 17 0118 E6 22 011A 20 00 01 011D 4C 90 00		INCZ JSR	CNTR DISP	INCR CNTR
	INTERR	UPT RO	UTINES	
0120 48 0121 8A 0122 48 0123 08	VCTL	PHA TXA PHA PHP		SAVE A, X AND STATUS ON STACK
0124 AD 00 17 0127 48 0128 29 60 012A F0 0F 012C 68 012D C8		LDA PHA ANDIM	\$60	READ KEYBOARD SAVE ON STACK MASK ALL BUT TOP BITS CONTROL CHARACTER? NO. RECALL A AND INCR Y
012E 99 00 02 0131 20 00 01 0134 C6 22	BACK .	STAY JSR DECZ		STORE A CHAR IN FIFO DISPLAY CNTR IF LESS THAN 10 UPDATE CNTR RESTORE REGISTER RETURN FROM INTERRUPT
013B 68 013C 29 7F 013E C9 02	CNTRL	ANDIM	\$7F	RECALL A FROM STACK MAKS OFF HIGHEST BIT BACKSPACE?

0140 D0 06 0142 88 0143 E6 22 0145 4C 36 01	I J	DEY INCZ	CNTR	TEST OTHER CHARACTER YES. DECR Y TO DELETE CHARACTER FIX COUNTER RETURN
014A DO 58 014C A9 58	I 2 1 2	BNE LDAIM STA LDAIM STAZ	ARND FIX NMIL \$00 CHEK	
0158 48 0159 8A 015A 48 015B 08	T F	PHA TXA PHA PHP		SAVE REGISTERS
015C AD 00 17 015F 29 0F 0161 AA 0162 A5 24 0164 C9 01	I	LDA ANDIM TAX LDAZ CMPIM BEQ TXA ASLA STAZ ASLA CLC ADCZ STAZ INCZ	\$0F CHEK \$01 AHD SPEED SPEED SPEED	TIMES 2 SAVE TIMES 4 TIMES 8 PREPARE TO ADD SPEED *8 + *2 = *10 STORE SET FOR SECOND DIGIT
0178 C6 24 017A 8A 017B 18 017C 65 13 017E 85 13 0180 38 0181 A2 00 0183 A9 94 0185 85 1E 0187 A9 04 0189 85 1F 018B A5 1E 018D E5 13 018F 85 1E 0191 A5 1F 0193 E9 00 0195 85 1F 0197 E8 0198 B0 F1 019A 86 00 019C A9 20 019E 8D FA 17	UP I S S S S S S S S S S S S S S S S S S	TXA CLC ADCZ STAZ SEC LDXIM LDAIM STAZ LDAIM STAZ LDAZ SBCZ STAZ LDAZ SBCZ SBCZ STAZ LDAZ SBCIM STAZ LDAZ LDAZ LDAZ LDAZ LDAZ LDAZ LDAZ LD	SPEED SPEED \$00 \$94 LO \$04 HI LO SPEED LO HI \$00 HI	ADD ONES DIGIT TO TENS DIGIT ANS STORE DIVIDE 494(HEX)/SPEED CLEAR X FOR QUOTIENT LOW ORDER BYTE OF DIVIDEND HIGH ORDER BYTE OF DIVIDEND START SUB. FROM DIVIDEND UNTIL BORROW FROM HIG BYTE, IE CARRY IS SET IF BORROW OCCURS FROM LOW ORDER BYTE, SUB 1 FROM HIGH ORDER BYTE INCR X FOR EACH SUB. BORROW FROM HI? NO. GO BACK AND SUB. OTHERWISE DONE RESET NMI VECTORS FOR VCTL

O1A4 C9 12 ARND CMPIM \$12 REMAINDER OF VCTL O1A6 DO 03 BNE TREE CONTROL R? O1A8 4C 00 03 JMP RCV YES. GO TO RECEIVE PROGRAM O1AB C9 0D TREE CMPIM \$0D CARRAIGE RETURN? O1AD DO 03 BNE BUF BRANCH IF NOT O1AF 4C 5B 00 JMP RTN YES. START MAIN PROGRAM O1B2 C9 07 BUF CMPIM \$07 CONTROL G? O1B4 FO 03 BEQ BRR YES. RESET STACK POINTER AND GO O1B6 4C B6 01 IDLE JMP IDLE TO LOOP. OR, IDLE HERE O1B9 A2 FF BRR LDXIM \$FF WHILE BUFFER IS LOADED O1BC 4C 90 00 JMP LOOP AND CONTINUE	01A1 4C 36 01	JMP	BACK	RETURN TO MAIN PROGRAM
	01A6 D0 03 01A8 4C 00 03 01AB C9 0D 01AD D0 03 01AF 4C 5B 00 01B2 C9 07 01B4 F0 03 01B6 4C B6 01 01B9 A2 FF 01BB 9A	BNE JMP TREE CMP BNE JMP BUF CMP BEQ IDLE JMP BRR LDX TXS	TREE RCV IM \$0D BUF RTN IM \$07 BRR IDLE IM \$FF	CONTROL R? YES. GO TO RECEIVE PROGRAM CARRAIGE RETURN? BRANCH IF NOT YES. START MAIN PROGRAM CONTROL G? YES. RESET STACK POINTER AND GO TO LOOP. OR, IDLE HERE WHILE BUFFER IS LOADED RESET STACK TOP

MORSE CODE RECEIVE PROGRAM

ORG \$0	3	00
---------	---	----

0300 A9 90	RCV	LDAIM	IRQ	SET IRQ VECTORS
0302 8D FE 17		STA	IRLO	
0305 A9 03		LDAIM	IRQ	/ PAGE ADDRESS
0307 8D FF 17		STA	IRHI	
030A A5 00	CRK	LDAZ	TIME	SET DOT LENGTH BY GETTING
030C 4A		LSRA		TIME AND DIVIDING BY 2
030D 85 51		STAZ	HALFT	
030F 46 51			HALFT	HALFT HALFT IS 1/2 DOT LENGTH
0311 85 52				
0313 06 52		ASLZ	TWOT	TWOT IS TWICE DOT LENGTH
0315 85 53			FIVET	
				MULTIPLY BY 4
0318 OA				
0319 18		CLC		
031A 65 53		ADCZ	FIVET	AND ADD 1 TIMES TO GET
031C 85 53		STAZ		5 TIMES DOT LENGTH
031E A9 00			\$00	CLEAR MARK AND SPACE
0320 85 54			MCNTZ	COUNTERS
0322 85 EE		STAZ	SCNTZ	
0324 58		CLI		ALLOW INTERRUPTS TO START
0325 A2 01			\$01	INIT CHARACTER REGISTER
0327 4C 27 03	IDL	JMP	IDL	IDLE HER UNTIL MARK OCCURS
032A 20 8A 03		JSR	TIMSET	START TIMER FOR SPACE COUNT
032D E6 EE		INCZ		INCR SPACE COUNTER
032F A5 EE		LDAZ	SCNTZ	DOES IT EXCEED 1/2 DOT LENGTH?
0331 C5 51		CMPZ	HALFT	
0333 B0 08		BCS	CHECK	YES, JUMP TO SET CHAR REGS
0335 AD 07 17	TIAW	LDA	TMER	OTHERWISE WAIT FOR TIMER
0338 10 FB		BPL	WAIT	
033A 4C 2A 03		JMP	AGN	AND COUNT SPACES
033D 8A	CHECK	TXA		SHIFT CHAR REGISTER LEFT
033E 0A		ASLA		
033F AA		TAX		

0340 A5 54 LDAZ MCNTZ IF MARK COUNTER EXCEEDS '	TWICE
0342 C5 52 CMPZ TWOT THE DOT LENGTH, PUT ONE 10344 90 03 BCC SKIP CHAR REGISTER, OTHERWISE 0346 E8 INX 0347 B0 11 BCS FAT IF A DASH, SKIP DISPLAY	IN
0344 90 03 BCC SKIP CHAR REGISTER, OTHERWISE	A ZERO
0346 E8 INX	
0347 BO 11 BCS FAT IF A DASH, SKIP DISPLAY	
AND AND COURT AND A TO A DOME COMPAND DITTOR OF	IME
034A C5 00 CMPZ TIME FOR SPEED INDICATOR	
034C BO 07 BCS CAT	
034E A9 F1 LDAIM \$F1 SHOW "F" IS DISPLAY	
0350 8D 40 17 STA SAD	
0353 00 05 RCC FAT	
0355 A9 ED CAT LDAIM \$ED SHOW "S" IN DISPLAY	
0357 8D 40 17 STA SAD	
035A A9 00 FAT LDAIM \$00 CLEAR MARK COUNTER	
035C 85 54 STAZ MCNTZ	
0361 10 FR BPL HOLD	
0363 20 8A 03 .ISR TIMSET START TIMER AGAIN	
0366 F6 FF TNC7 SCNT7 INCR SPACE COUNTER AGAIN	
O368 A5 EE LDAZ SCNTZ	
0364 C5 52 CMPZ TWOT DOES SPACE COUNTER EXCEE	D TWICE
035E AD 07 17 HOLD LDA TMER WAIT FOR TIMER 0361 10 FB BPL HOLD 0363 20 8A 03 JSR TIMSET START TIMER AGAIN 0366 E6 EE INCZ SCNTZ INCR SPACE COUNTER AGAIN 0368 A5 EE LDAZ SCNTZ 036A C5 52 CMPZ TWOT DOES SPACE COUNTER EXCEE 036C 90 F0 BCC HOLD THE DOT LENGTH. IF NOT, 036E 20 CA 03 JSR CHAR IF YES, PRINT CHARACTER 0371 A2 01 LDXIM \$01 RESET CHAR REGISTER	HOLD
O36E 20 CA O3 JSR CHAR TE VES PRINT CHARACTER	.1020
0371 A2 01 LDYTM \$01 RESET CHAR REGISTER	
O373 AD O7 17 DOZE LDA TMER WATT FOR TIMER	
0376 10 FR RPI DOZE	
OSTO TO TE STEE SOME TARET START TIMER AGAIN	
OSTO ZO OR OS SON TIMBEL STANT TIMEN ROALN OSTO FOR FR TNC7 SCNT7 TNCR SPACE COUNTER	
OSTD AS FE INAT SCHOOL STREET COUNTER	
OSTE C5 52 CMP7 FIVET DOES SPACE COUNTER FYORE	פאדעה הנאהפ
0381 00 FO BCC DOZE DOT FRICTH TE FESS DO	7F AGATN
0371 A2 01 LDXIM \$01 RESET CHAR REGISTER 0373 AD 07 17 DOZE LDA TMER WAIT FOR TIMER 0376 10 FB BPL DOZE 0378 20 8A 03 JSR TIMSET START TIMER AGAIN 037B E6 EE INCZ SCNTZ INCR SPACE COUNTER 037D A5 EE LDAZ SCNTZ 037F C5 53 CMPZ FIVET DOES SPACE COUNTER EXCEE 0381 90 F0 BCC DOZE DOT LENGTH. IF LESS, DO 0383 20 CA 03 JSR CHAR OTHERWISE PRINT SPACE 0386 78 SEI PREVENT INTERRUPTS WHILE 0387 4C 0A 03 JMP CRK CHECKING SPEED SETTING	DE AUATN
0386 78 SET PREVENT INTERRIPTS WHILE	
0387 4C OA 03 JMP CRK CHECKING SPEED SETTING	
0307 40 08 03 OMF CHR CHECKING SPEED SETTING	
038A A9 20 TIMSET LDAIM \$20 LOAD TIMER FOR 2.048 MS	
038C 8D 06 17 STA TIM	
038F 60 RTS RETURN TO RCV PROGRAM	
ogor oo kib kibink to kot i kodkan	
0200 00 TRO BUD GAVE REGIGERRO	
0390 08 IRQ PHP SAVE REGISTERS	
0391 48 PHA	
0392 20 8A 03 JSR TIMSET START TIMER	
OCCUPATION AND THE TOTAL THREE	
0395 AD 07 17 LOAF LDA TMER WAIT FOR TIMER	
0398 10 FB BPL LOAF	
039A AD 02 17 LDA PBD IS MARK SIGNAL PRESENT	
039D 10 09 BPL OVER YES, GO TO OVER	
039F A9 00 LDAIM \$00 NO, MUST HAVE BEEN NOISE	
039F A9 00 LDAIM \$00 NO, MUST HAVE BEEN NOISE 03A1 85 54 STAZ MCNTZ WHICH CAUSED INTERRUPT.	RETURN
039F A9 00 LDAIM \$00 NO, MUST HAVE BEEN NOISE 03A1 85 54 STAZ MCNTZ WHICH CAUSED INTERRUPT. 03A3 E6 EE INCZ SCNTZ TO COUNT SPACE AFTER RES	RETURN
039F A9 00 LDAIM \$00 NO, MUST HAVE BEEN NOISE 03A1 85 54 STAZ MCNTZ WHICH CAUSED INTERRUPT. 03A3 E6 EE INCZ SCNTZ TO COUNT SPACE AFTER RES 03A5 68 PLA MARK COUNTER TO ZERO	RETURN
039F A9 00 LDAIM \$00 NO, MUST HAVE BEEN NOISE 03A1 85 54 STAZ MCNTZ WHICH CAUSED INTERRUPT. 03A3 E6 EE INCZ SCNTZ TO COUNT SPACE AFTER RES	RETURN

03B7 AD 07 17 03BA 10 FB 03BC AD 02 17	KILTIM	INCZ LDAZ CMPZ BCC LDAIM STAZ LDA BPL LDA BPL TXA LDXIM TXS TAX	MCNTZ MCNTZ HALFT LOAF \$00 SCNTZ TMER KILTIM PBD OVER \$FF	START TIMER AGAIN INCR MARK COUNTER DOES MARK COUNTER EXCEED 1/2 THE DOT LENGTH? NO, GO LOAF AND CHECK MARK YES. CLEAR SPACE COUNTER CHECK TIMER KILL TIME CHECK MARK SIGNAL ON PB7 LOOP AGAIN IF STILL ON SAVE S WHILE STACK POINTER IS SET RESET TO TOP OF STACK RESTORE X CLEAR INTERRUPT FLAG SET EARLIER RETURN TO COUNT SPACE
03D1 2D F9 13 03D4 C9 3F 03D6 90 11 03D8 A9 1F 03DA 2D FA 13 03DD 18 03DE 69 01 03E0 C9 20	UP	STA LDAIM AND CMPIM BCC LDAIM	DATA \$3F CULO \$3F AHD \$1F CUHI \$01 \$20 UP \$10 CUHI	
	SEND SU	JBROUT]	NE	
1780 44	CEND	ORG	\$1780	A CONTAINS CUAD EDOM ETEO
1780 AA 1781 B5 00 1783 30 3F 1785 18 1786 A2 08 1788 2A	RPT	TAX LDAZX BMI CLC LDXIM ROLA	WDSP \$08	A CONTAINS CHAR FROM FIFO USE THIS TO LOOKUP MORSE SPACE BAR CHAR HAS 1 IN BIT 7 IF NOT MINUS, CLEAR CARRY FLAG AND SET UP X FOR 8 ROL INSTRUCTIONS ROTATE LEFT UNTIL 1 APPEARS IN CARRY
1789 BO 06 178B CA 178C FO 35 178E 4C 88 17 1791 CA 1792 2A 1793 48 1794 8A 1795 48	DWN BACK	BCS DEX BEQ JMP DEX ROLA PHA TXA	DWN OUT RPT	BRANCH IF 1 IN CARRY ELSE, DECREMENT X IF X = 0, THEN DONE ELSE CONTINUE KEEP TRACK OF BITS TESTED ROTATE A LEFT AND SAVE ON STACK SAVE X ON STACK ALSO

1796	во	18			BCS	DASH	DID ROTATE SET CARRY? IF YES,
1798	A2	01			LDXIM	\$ 01	SEND DASH. ELSE SEND DOT
179A	EE	02	17	DAH	INC	PBD	PBO WILL BE LOGICAL 1 FO 1 T
179D	20	C9	17				TIME GIVES DELAY OF TIME (1.024MS)
17A0	CA				DEX		ONE TIME UNIT IS UP
17A1	DO	FA			BNE	SPA	IS X = 0? DELAY ANOTHER UNIT
17A3	AD	02	17		LDA	PBD	YES. NOW CHECK PBO. IF A 1
17A6	4 A				LSRA		A SHIFT WILL SET CARRY FLAG
17A7	90	OC			BCC	DONE	IF CARRY CLEAR, THEN DONE
17A9	CE	02	17		DEC	PBD	OTHERWISE, SET PBO = 0 FOR ELEMENT
17AC	E8				INX		SPACE FOR A DELAY OF 1 UNIT BY
17AD	4C	9D	17		JMP	SPA	SPACE FOR A DELAY OF 1 UNIT BY RESETTING X AND LOADING TIMER
17B0	A2	03		DASH	LDXIM	\$ 03	DASH TAKES 3 TIME UNITS
17B2	4C	9A	17		JMP	DAH	DASH TAKES 3 TIME UNITS SEND 3 UNITS FOLLOWED BY SPACE
17B5	68			DONE	PLA		THEN ELEMENT IS DONE SO RESTORE A AND X AND GO BACK IF X IS NOT ZERO
17B6	AA				TAX		RESTORE A AND X AND GO BACK
17B7	68				PLA		IF X IS NOT ZERO
							OTHERWISE ADD CHARACTER SPACE
17B9	DO	D7			BNE	BACK	BY RUNNING TIMER FOR
							2 MORE TIME UNITS
				AGAIN			
17C0	CA				DEX		
1701	DO	F'A		0.11m	BNE	AGAIN	IF X = 0, THEN DONE OR ELSE DELAY MORE
1703	60				RTS		OR ELSE DELAY MORE
170	4.0	Δlı		LIDOD	LDVIN	A O II	HODDONACE PROUTERS IN MORE WINE UNITED
1704	HZ	DD	17	WDSP	PDYTM	ACATN	WORDSPACE REQUIRES 4 MORE TIME UNITS SO USE TIMER FOR THIS
1700	40	עם	17		JMP	AGAIN	SO USE TIMER FOR THIS
1700	45	00		TIMER	1047	TIME	GET TIME FROM ZERO PAGE
				TIMEN			LOAD DIVIDE BY 1024 TIMER
							IS TIMER FINISHED?
17D1							
17D3					RTS	Jiii	NO, WAIT FOR IT YES, RETURN
., 25	-						,

APPENDIX: Using the KIM-1 Ports to Output the ASCII

Most readers will not have the same addressable video system used by the author. To use the receive portion of the program, some provision must be made to output the ASCII along with a strobe pulse. Below you will find a suggested program to do this. It makes use of ports SAD and SBD addresses 1740

and 1742 respectively. These are available on the application connector. The ASCII code appears at the KB COL A-G pins, while the strobe should appear at the TTY PTR pin.

NOTE: While this program should work it has not been tested.

ALTERNATIVE ASCII OUTPUT

ORG \$03CA

*** THIS ROUTINE HAS NOT BEEN TESTED ***

03CA 03CA 03CA 03CA					* * *	\$0000 \$1740 \$1741 \$1742 \$1743	
03CA	A 9	20		CHAR	LDAIM	\$20	ENABLE OUTPUT PULSE PINS
03CC	8D	42	17		STA	SBD	
03CF	A9	21			LDAIM	\$21	
03D1	8D	43	17		STA	SBDD	
03D4		40	17		LDA	SAD	SAVE CONTENTS OF CURRENT
03D7					PHA		DISPLAY ON KIM-1
03D8		41	17		LDA	SADD	
03DB					PHA		
03DC						ZTB	GET ASCII CODE
03DE			17		STA	SAD	OUTPUT ASCII
03 E 1					LDAIM	•	
03 E 3					STA	SADD	ENABLE OUTPUT PORT
03 E 6		42	17		INC	SBD	STROBE PULSE WILL BE
03 E 9					NOP		LENGTHEN PULSE
03 E A		42	17		DEC	SBD	NEGATIVE
03ED					PLA		RESTORE SADD AND SAD
03 EE		41	17		STA	SADD	
03F1		1			PLA	245	
03F2			17		STA	SAD	DECEMBER CORD AND CORD
03F5	_					\$1E	RESTORE SBDD AND SBD
03F7					STA	SBDD	
03FA					LDAIM		
03FC		42	17		STA	SBD	
03FF	60				RTS		

PET SOFTWARE FROM COMMODORE

Roy O'Brien P.O. Box 187 Somerset, NJ 08873

It appears that in response to specific questions, Commodore is sending out selected Application Notes. The software consists of the following:

Machine Language Monitor - (9 pages) A discussion of the TIM program as adapted to the PET. Early PET owners are supposed to receive TIM on cassette and later PETs will have TIM in ROM.

PET Cassette Files - (31 pages) A learn-by-doing mini-course in file management with the PET.

IEEE-488 Devices - (5 pages) A listing of available equipment which directly interfaces to the PET. Gives device, model number, manufacturer; includes printers, counters, measurers, ADCs, DACs, timers, synthesizers, analyzers, plotters, tapes, discs, etc.

BASIC Bugs - (4 pages) Kinks, quirks and bugs in PET BASIC.

PET and ASCII - (4 pages) Definitions and symbol codes, including a neat little program which shows graphics and codes on screen.

PET Uses Its Memory - (1 page) A reprint of PET memory usage from PCCs Nov/Dec 1977 issue.

Animating Your PET - (2 pages) How to use the programmable cursor controls to create moving graphics.

Some Questions and Answers - (11 pps) Things you always wanted to know and weren't afraid to ask; summarized. must for PET owners.

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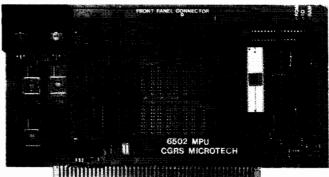
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Throughout the five months I've had my PET, I've felt the biggest design oversight was leaving out a speaker. Commodore even went to the trouble of removing one, along with its amplifier, from the tape drive.

The versatility of the Apple II's audio output is nice, but I'd be satisfied with a simple beeper like the one in the Heath Company's H8. That's why I'm spending \$19.95 for the PETsqueak from HUH Electronic Music Productions (P.O. Box 259, Fairfax, CA 94930 415/457-This assembled and tested de-7598). vice doesn't just produce audible output under user control. It also beeps automatically during program loading or saving to indicate file headers and completion of the operation. I look forward to being able to turn away from my PET and still keep track of what's happening.

PET-compatible products from HUH scheduled for April and May delivery include an 8-bit digital-to-analog converter, an adapter for a video monitor (so you can have a larger screen facing a different direction), and an S-100 bus interface.

While I'm looking forward to adding the beeper to my PET, the thing that will really enhance its value is a compatible printer. The big news this month is that you can now hook any RS-232 printer to your PET. The necessary adapter is sold by Connecticut microComputer (150 Pocono Rd., Brookfield, CT 06804). Assembled and tested, but without power supplies, case, or RS-232 connector, it goes for \$103.50 with shipping and handling. The complete version is \$174. The speed will be set at 300 baud unless another rate is requested at the time of ordering. This may be changed by the user later. With the PET ADApter model 1200 you can produce not only program listings, but

also mailing labels, letters, etc. The appearance will naturally depend on the printer used. Lower case letters are substituted for the graphics character.

The third addition I plan to make to my PET is a 6502 assembler written in BASIC. I ordered this for \$24.95 from Personal Software (P.O. Box 136-M3, Cambridge, MA 02138 617/783-0694).

While I'm content with the PET keyboard anyone who wants to hook up another one may be interested in the ASCII keyboard interface sold by Excel Co. (2241 Tamalpais Ave., El Cerrito, CA 94530 415/237-8114). Prices start at \$65.

The makers of the KIMSI have announced the PETSI. In kit form with one S-100 connector it's \$105. Assembled with the maximum of four S-100 slots it's \$165. Neither version includes a power supply. Forethought Products (P.O. Box 386-D, Coburg, OR 97401 503/485-8575) is the manufacturer.

May delivery is scheduled for an RS-232 interface from The Net Works (5014 Narragansett #6, San Diego, CA 92107 714/223-1176). Single port version is \$240; dual port \$280.

The PET Vet will have more to say about these and other PET oriented products in future issues of MICRO. If you have information about PET products, as a manufacturer, dealer, or user, please send materials to:

The PET Vet
MICRO
P.O. Box 3
S. Chelmsford, MA 01824

THE MICRO SOFTWARE CATALOG

Mike Rowe P.O. Box 3 S. Chelmsford, MA 01824

As a service to the 6502 community, MICRO will publish a continuing catalog of software available for 6502 based systems. The source of this information will normally be the authors or distributors of the software. Since there is only a limited amount of space which can be devoted to this effort, there will be some restrictions placed on what is published. To qualify for inclusion in the catalog the software must be currently available, should have been sold (or given) to at least twenty-five customers, must be of general interest, and must be significant. "Significant" means that the program is not just a short utility which could be presented as a one-page article in a magazine, or a simple game, etc. The intent of the catalog is not to promote everyone selling everything, but rather to highlight the important software packages which do exist.

Publication of information about any software in this catalog does not imply anything about its worth, capabilities, documentation, etc. We depend on the information supplied to us. We will not knowingly include any software that is not worthy, and we reserve the right to publish additional information about these products - be it good or bad - that we receive from our readers or any other valid source.

It is easy to get your package listed. Just write to the above address and provide the information required as shown in the listings below. Please write your own "description". If we have to write the description from general information you provide, we may miss points which you think are important and emphasize things you think are trivial. Also, material which is presented in the proper form will normally get priority over other material.

Name: ASSM/TED

System: Preconfigured for TIM

Can be modified for other systems.

Memory: 4K RAM Language: Assembler

Hardware: CRT and Keyboard, tapes and

printer optional.

Description: A resident Assembler/Text Editor. Syntax very similar to MOS Technology. Produces relocatable object code on tape and can store directly executable code in memory during assembly. Programs can be assembled from memory of tape. Includes 17 operating commands and 16 pseudo ops. Editor has auto line numbering, file formating, and a manuscript feature.

Copies: Information not provided.

Price: \$25.00

Includes: Hex Dump of ASSM/TED and Relocating Loader, and Operators Manual. No tape provided.

Ordering Info: Specify memory limits: 0200-1200, 0400-1400, 1000-2000, or 2000-3000. Select one.

Author: C. W. Moser

Available from:

C. W. Moser 3239 Linda Drive

Winston-Salem, NC 27106

Name: COSMAC 1802 Simulator

System: KIM-1

Memory: Less than 1K RAM

Language: Assembler Hardware: Basic KIM-1

Description: Permits the KIM-1 to simulate the COSMAC 1802 by executing its instruction set. The simulator does this by interpretting the COSMAC instructions in a normal program sequence and making all internal COSMAC registers available for examination at any time. They may be viewed statically in a single step mode or dynamically in a trace mode. All COSMAC software features are supported with the exception of DMA.

Copies: Just released. Will be discussed in an article in Kilobaud.

Price: \$10.00

Includes: KIM-1 cassette tape, user
 manual, and complete source listing.

Ordering Info: None required

Author: Dann McCreary

Available from:

Dann McCreary

4758 Mansfield St, #2M San Diego, CA 92116 Name: PLEASE

System: Basic KIM-1

Memory: Basic KIM-1 memory Language: Assembler/PLEASE

Hardware: Basic KIM-1

Description: A collection of games and Includes a 24 hour clock, HiLo game, Mastermind, Shooting Stars, Drunk Test, Reaction Time Tester, Adding Machine, and more. Written in a "high-level" language - PLEASE. Permits the user to modify and create his own pro-Let's you show off your KIM-1,

and teaches you how to use it. Copies: Over 800 have been sold

Price: \$15.00

Includes: Operators manual, complete source listings, PLEASE language description, with object code on Hyper-

Ordering Info: None Author: Robert M. Tripp

Available from: The COMPUTERIST P.O. Box 3

S. Chelmsford, MA 01824

Name: Micro-ADE

System: KIM-1 (easily modified for use with other 6502 based systems)

Memory: 8K RAM or 4K EPROM + 4K RAM

Language: Assembler

Hardware: Terminal - CRT or TTY, cas-

sette units optional

A combination Assembler Description: Editor, and Disassembler. Uses MICRO 6502 syntax. With automatic cassette controls, any length file may be edited and assembled. Object files may be automatically dumped to cassette and for short programs may be dumped to and executed from memory. Includes many useful commands for handling cassettes, moving data in memory, and so forth. Copies: Hundreds

\$25.00 without source listings

\$25.00 for source listings

Includes: Extensive user manual which includes source listings for the I/O to permit user modification. Object on Hypertape cassette.

Ordering Info: Specify with or without the optional source listings.

Author: Peter Jennings

Available from:

Micro-Ware Ltd. 27 Firstbrooke Road Toronto, Ontario Canada M4E 2L2

The COMPUTERIST P.O. Box 3 S. Chelmsford, MA 01824 Name: The 6502 Program Exchange

System: TIM and KIM-1 Memory: Depends on Program

Language: Assmebler, BASIC, FOCAL

Hardware: Depends on Program

A large collection of Description: programs for 6502 based systems. These include utilities, games, subroutines, an assembler, editor, and a high level language: FOCAL.

Copies: Few to Many depending on the

particular program.

Price: Depends on program. Many are based purely on number of pages of code. Major packages are priced

separately.

Includes: Normally includes source listings, documentation, sheets of sample run, and paper tape. KIM-1 cassettes at no additional charge if user supplies cassettes.

Ordering Info: Write for catalog. Author: Many different authors.

Available from:

The 6502 Program Exchange 2920 Moana Reno, NV 89509

Name: Personal Savings Investment

Loan Repayment

Direct Reduction Loan Info.

System: APPLE II At least 16K Memory: Language: APPLESOFT BASIC Hardware: Standard APPLE II

Description: Three separate programs. PSI - compute future value of your investments; monthly amount needed to get to a certain goal at a certain time. LP - determine monthly payments for a car, house or other type of load.

DRLI - find the total interest paid and remaining balance is for a loan.

Copies: Over 25 combined

Price: \$3.75 (including handling) each of the three programs.

Includes: Object on cassette tape. listing of the program and examples of program useage.

Ordering Info: Specify which program.

Author: Les Stubbs

Available from: Les Stubbs

23725 Oakheath Place Harbor City, CA 90710 Name: TINY BASIC

System: KIM, TIM, Jolt, Apple I Memory: Minimum of 3K

Language: Assembler

Hardware: User defines I/O

Description: TINY BASIC is a subset of regular BASIC, limited to 16-bit integer arithmetic [+, -, *, /, ()]. There are 26 variables (A-Z), no stirngs and The following commands are no arrays. functional: LET PRINT INPUT IF-THEN GOTO GOSUB RUN LIST CLEAR RETURN REM END. TINY BASIC does not contain any I/O instructions; three JMPs link TINY to the user's I/O routines. These are well documented in the manual.

Copies: "Several hundred 6502 version"

Price: \$5.00

Includes: 26 page User Manual and a paper tape in standard hex loader format. Hex Dump may be substituted upon request for paper tape.

Ordering Info: Specify version:

TB650K (0200-0AFF) KIM, TIM, TB650J (1000-18ff) Jolt

TB650T (2000-28FF) KIM with 4K RAM

Author: Tom Pittman

Available from:

ITTY BITTY COMPUTERS

P.O. Box 23189 San Jose, CA 95153

Name: HELP Mailing List Package

System: Basic KIM-1 Memory: Basic KIM-1 Language: Assembler/HELP

Hardware: Terminal, Cassettes, Relays Description: A complete package for creating, maintaining, and printing mailing list information. A high speed cassette routine reads/writes at 800 baud (twelve times the KIM-1 rate) and can store about 900 names on one side of a 60 minute tape. Selective printing of mailing list. This package is used to maintain the MICRO mailing list package is written in HELP, a "high-level" language which makes it easy to customize the package for your own requirements.

Copies: Over 100 Price: \$15.00

Includes: An extensive user manual, a detailed discussion of the HELP language, and complete source

listings. Object on Hypertape.

Ordering Info: None Author: Robert M. Tripp Available from:

The COMPUTERIST P.O. Box 3

S. Chelmsford, MA 01824

Name: ASM/TED

System: KIM-1 (may be modified for use

with other 6502 based systems)

Memory: 6K RAM Language: Assembler Hardware: TTY

Description: The text editor performs line editing in RAM and can dump/load to paper tape or audio cassette. resident assembler is single-pass using the standard MOS Technology syntax. Source code may be paper tape or memory resident and object code is always to memory.

Copies: Information not provided.

Price: \$70.00

Includes: 50 page manual, source listings, and object on KIM cassette or paper tape.

Ordering Info: Send \$2.00 for current catalog of available software.

Author: Not specified

Available from:

ARESCO

450 Forest Ave., Q-203 Norristown, PA 19401

Name: MicroChess System: Basic KIM-1 Memory: Basic KIM-1 Language: Assembler Hardware: Basic KIM-1

Description: Plays a reasonably good game of chess on a basic KIM-1. programmed openings. User enters his move via the KIM keypad and the KIM The computer Display shows the move. then makes its move and displays it. Program may be set to play at different speeds: 3, 10, or 100 seconds per move A great way to demo your KIM. average.

Copies: Hundreds \$10.00 without cassette Price: \$15.00 with cassette

Includes: Operator's manual, source listings, and a detailed discussion of the operation of the program.

Object on cassette tape optional. Ordering Info: Specify tape or not.

Author: Peter Jennings Available from:

Micro-Ware Ltd. 27 Firstbrooke Road Toronto, Ontario Canada, M4E 2L2

The COMPUTERIST P.O. Box 3 S. Chelmsford, MA 01824

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APPLE II PRINTING UPDATE

C. R. (Chuck) Carpenter W5USJ 2228 Montclair Place Carrollton, TX 75006

"Printing with the Apple II" [MICRO #3] included information that has been revised. Since the article was written, I've improved some things and I'd like to pass them along.

The Adapter Didn't

After using the adapter circuit for a couple of months, I took a good look at what was happening. The conclusion was nothing! Initially, it didn't work when I connected it to the RS-232 receiver on the PS-40. I connected it to the serial TTL input (pin A7) and it worked. The voltage swing wasn't excessive (clamped with some diodes), so I left it hooked-up. Should have been a clue. But at the time I didn't see it, and anyway, it worked.

During one of our (infrequent) snowedin days here in Texas, I had time to
think about it. There wasn't any apparent reason not to hook it up directly; and I did. It worked the way it
should so I had a no-interface-required
computer to printer system. When I received my new Apple Operator's Manual I
noticed a new interface circuit, not
the one I used as originally provided.

All that is needed is to connect a signal lead and ground from the Apple to the printer. The signal lead connects to Pin 15 of Apple's game paddle connector. Also to Pin A7, TTL serial data in, on the printer. I soldered the game paddle connector to the 16 pin header. No other connections needed.

Now You Can Start and Stop

Ted Spradley, a programmer/engineer at work, helped me with the machine language print program. His analysis suggested restoring the page zero registers to make the print routine stop. As you more experienced programmers would know, it worked. I rewrote the program to store and restore the page zero data and now the routine turns on and off under program control. The program, shown in Figure 1, was a revelation to me. Again, my thanks to Ted for his assistance.

The Blues Are Gone

Most of my programs are printed on the paper that turns blue (and fades). Telpar has a black on off-white paper now. This new paper makes a much sharper copy too. The blue paper was also susceptible to smearing. This did not help the copy quality either, photographically or Xerographically.

There! Now that the problems are resolved, what's holding you back? Let's get printing.

Author's Note: Even if you don't have a printer, the print routine is useful. Use it to slow the screen speed down. This way you can read a listing during a slow scroll.

Getting Decimal Values From Hex Data

For some other program, POKE was used to enter machine language from BASIC. I did this for the print routine. All the HEX values have to be converted to decimal. At first I did this with the TI Programmer. Then I "discovered" what PEEK is all about. A BASIC program to print the decimal values simplifies the job. Convert the first and last addresses (to do a range of addresses) to their decimal values These values are 875 and 967 for the print program. Then use them in a FORNEXT routine like this:

100 FOR I=875 TO 967:PRINT PEEK(I);: PRINT" ";:NEXT I:END

This reduced a two hour job to about ten minutes. Hooray for progress.

*36BLLL		*36B.3C7
036B- A5 36 036D- 8D 06 03 0370- A5 37 0372- 8D 07 03 0375- A9 89 0377- 85 36 0379- A9 03 037B- 85 37 037D- 60 037E- AD 06 03 0381- 85 36 0383- AD 07 03 0388- 60 0389- 84 35	LDA \$36 STA \$0306 LDA \$37 STA \$0307 LDA #\$89 STA \$36 LDA #\$03 STA \$37 RTS LDA \$0306 STA \$36 LDA \$0307 STA \$37 RTS STA \$37	036B- A5 36 8D C6 03 0370- A5 37 8D C7 03 A9 89 85 0378- 36 A9 03 85 37 60 AD C6 0380- 03 85 36 AD C7 03 85 37 0388- 60 84 35 48 20 A5 03 68 0390- C9 8D D0 0C A9 8A 20 A5 0398- 03 A9 58 20 A8 FC A9 8D 03A0- A4 35 4C F0 FD A0 0B 18 03A8- 48 B0 05 AD 58 C0 90 03 03B0- AD 59 C0 A9 D3 48 A9 20 03B8- 4A 90 FD 68 E9 01 D0 F5 03C0- 68 6A 88 D0 E3 60 F0 FD
038B- 48 038C- 20 A5 03 038F- 68 0390- C9 8D 0392- D0 0C 0394- A9 8A	PHA JSR \$03A5 PLA CMP #\$8D BNE \$03A0 LDA #\$8A	Dudud. Davida a
0396- 20 A5 03 0399- A9 58	JSR \$03A5 LDA #\$58	Print Routine
039B- 20 A8 FC 039E- A9 8D	JSR ≢FCA8 LDA #≸8D	START Print STOP Print
03A0- A4 35	LDY \$35	*36BG *37EG
03A2- 4C F0 FD 03A5- A0 0B 03A7- 18	JMP \$FDF0 LIY #\$0B CLC	>CALL 875
03A8- 48 03A9- B0 05 03AB- AD 58 C0	PHA BCS \$03B0 LDA \$C058	Type in one of above and then type RETURN to activate the command.
03AE- 90 03 03B0- AD 59 C0 03B3- A9 D3	BCC \$03B3 LDA \$C059 LDA #\$D3	<pre># = from Apple Monitor > = from Integer BASIC] = from Applesoft BASIC</pre>
03B5- 48 03B6- A9 20 03B8- 4A	PHA LDA #\$20 LSR	Change 03B4 to 4D for 300 baud.
03B9- 90 FD	BCC \$03B8	
03BB- 68 03BC- E9 01 03BE- D0 F5 03C0- 68 03C1- 6A 03C2- 88	PLA SBC #\$01 BNE \$03B5 FLA ROR DEY	
0303- D0 E3 0305- 60	BME \$03A8	
0306- F0 FD	RTS BEQ \$0305	

Figure 1

Listing and HEX Dump of Machine Language Print Routine



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MICRO STUFF

Mailing Labels

Barring unforseen difficulties (last May we lost electricity for four days due to a snow storm), the mailing label on your copy of MICRO will have been generated on a KIM-1 with a Diablo type printer and the HELP Mailing List Pack-Note near your name the two or three characters. The first two digits indicate the last issue you are scheduled to receive under your current subscription: 06 = issue number 6. The third character has particular meaning:

- X = your name will appear on any mailing lists we sell, unless you notify us to remove it;
- any other letter indicates you are getting MICRO free as an advertiser, exchange, or something;
- no letter indicates that your name will not be included in mailing lists we sell, per your request.

Our New Printer

This issue of MICRO is being printed by a new printing company. We anticipate that the quality will be as good as the previous work.

Deadlines

With our new printer (he's cheaper but takes longer), deadlines are even more important than before. All ADs must be received by May 14 for the June/July issue. Articles should be received as soon as possible.

Calendar/Directory

If enough information is provided to make it worthwhile, we can publish a regular Calendar of 6502 related events and a Directory of 6502 Clubs. Since MICRO is only published every other month, remember to give information for several months at a time.

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EMPLOYING THE KIM-1 AS A TIMER

- 3:5 020E should be A9 99 LDAIM \$99 since the processor is in decimal mode, not binary.
- 3:7 02A6 should be E4 03 not E0 03.

LIGHTING THE KIM-1 DISPLAY

Back cover There is no need to add Hex 80 to the sum of the individual LED segments to control PA7. It does hurt, but it is not required.

4:30

MICRO

STANDARD 6502 ASSEMBLY SYNTAX?

Hal Chamberlin 29 Mead Street Manchester, NH 03104

I could not help noticing the comment about MOS Technology's assembler syntax for the 6502 in MICRO #2. Judging from the force of that comment and the fact that every 6502 program I have seen uses a different assembler and systax there must be a great deal of discontent with MOS Technology's syntax.

Consideration of the history of 6502 development is all that is necessary to explain most of the features of its The designers initassembler syntax. ially worked at Motorola with the goal of incorporating leading features of the PDP-11 instruction set into the Later, after leaving Motorola and designing the 6502 for MOS Technology, their PDP-11 experience served as a model for an assembler syntax to adequately handle the 13 addressing modes and other features of their creation. The result is the syntax described in about 10 square inches on the 6502 card and illustrated by the KIM assembly listings we all practically know by heart. The PDP-11 is one of the most used minicomputers ever and I have not heard of any significant group of '11 users abandoning DEC's syntax even though it can become a little cryptic.

So let us take a close look at the MOS Technology syntax, iterate what is right about it, and see how we can live with those features that are less than ideal. Note that I am not at all against extensions of what they have defined but I think it is important that an assembler be able to correctly assemble the KIM source as printed.

First we have the assembler directives and other statements that have nothing to do with the instruction set. For the most part these have been lifted directly from the PDP-11 assembler manual. The distinguishing feature about these statements is that they are preceded by a period. I see nothing particularly wrong with these except perhaps that some of them are longer than three characters meaning that an opcode scanner might have to be a little more sophisticated than it would otherwise be. One definite problem though is the

method that must be used to reserve areas of memory for data storage. prefer the "DS 5" form rather than the ".=.+5" form for reserving five bytes probably because of an IBM background. But the real problem is that unless the assembler is carefully written, the location counter value printed to the left of such a statement gives the address of the first byte of memory used in the next statement rather than the address of the first byte of memory reserved in this one. However I think that the latter form can be lived with if one realizes that the expression ".=.+" is really the same as "DS" and provided the assembler prints the right address.

Now what about the machine instructions themselves? A tendency noted in several homebrew assemblers is to give every addressing mode variation of every instruction a different mnemonic. though this is a good advertising ploy to swell the 57 listed op codes into 151 "variations", it does not make good sense. The operation code should merely specify the operation and the operand column should specify the operands. In my way of thinking the addressing mode is part of the operand (it tells where the operand is) and not the operation. Of course MOS Technology violated this somewhat by putting the register designation in the op code but that is not nearly as bad as putting everything in the op code.

One particularly nice feature of the existing syntax is the specification of the two indirect addressing modes. designation "(SYMB, X)" clearly indicates that the value of SYMB is added to X before looking in the base page for the effective address and the designation "(SYMB), Y" says that the indirect cycle occurs before the contents of Y are added in to form the effective address. There should never be any problem with the use of parentheses for indicating indirect and the use of parentheses in arithmetic expressions. is unfortunate however that indexed addressing is of the form "SYMB, X" rather than "SYMB(X)" as on most other systems but it can certainly be lived with.

with respect to the other addressing modes, the assembler should take care of determining whether the "zero page" form or the "absolute" form is to be used. Essentially the assembler would look at the value of the address and if it is less than 0100 (hex), use the appropriate zero page addressing form of the instruction. Besides always insuring the shortest possible program (both space and time), it frees the programmer from learning many of the addressing mode restrictions of certain instructions. The assembler will flag an error only when it is physically impossible to perform the requested operation.

One last minor gripe is the field separators (colon after symbols and semicolon before comments) required which adds (slightly) to typing effort and uses three valuable print column positions. Of course this is also straight out of the PDP-11 assembler. I know a powerful assembler can be written without this requirement and still have free format (IBM 360 assembler) but my programmer friends say that explicit

delimiters can have important advantages. Anyway I live with it.

I can hear the cries now of "Sure it makes sense but it is so complicated to write a syntax analyzer for it". Of course our cross-town rivals (8080, Z-80) are already well into macro assemblers and linking relocating loaders and we are still working out the assembler syntax for our baby! If we believe that ours is a more powerful computer, surely an assembler with automatic address mode selection and conformance to our own manufacturer's assembly language is not too difficult a task to handle.

Editor's Note: While I do not want to use too much space in MICRO for debates on matters of personal preference, I will make space available in the next issue of MICRO for a rebuttal by a proponent of an alternative syntax. If no one writes such a rebuttal, I will do it myself, but I would much prefer to hear from one of you.

A WORM IN THE APPLE?

Mike Rowe P.O. Box 3 S. Chelmsford, MA 01824

There may be a serious problem hidden deep within the Apple II according to John Conway and Jack Hemenway of EDN magazine. As part of their system design project based on a bare-board Apple - "Project Indecomp" - they tried to interface a 6820 PIA to the Apple, and uncovered a potentially serious problem. The normal way to operate a 6502 based system is to provide an external clock [phase 0] to the 6502 which then generates two non-overlapping clock signal [phase 1 and phase 2] which are used to control all system timing. For some reason, the design of the Apple II violated this basic clock scheme and uses the phase 0 external clock instead of the 6502 generated phase 2 clock. While these two clocks

are very similar, they are not identical. Phase 1 and phase 0 have an overlap of about 50 nanoseconds. For many parts of the system this is not important, as indicated by the fact that the Apple II works. For other devices, however, such as the 6820 PIA, this difference is critical to the extent that the device simply will not work. A report in EDN scheduled for 20 May will cover this problem in detail, and we will try to get more info for the next issue of MICRO. Is the problem serious? Critical? Fatal? probably too early to judge the effect of this problem. It may not have an adverse effect in many systems. It may be possible to correct. Or it may be a very serious system problem.

WRITING FOR MICRO

One of the reasons I like the 6502 is that it seems to attract a lot of very interesting, active, enthusiastic users. I spend several hours on the phone each week talking to people who are so excited about what they are doing with their system that they just have to talk to someone. Oh, sometimes they pretend they have some "burning" question or want to order some small item, but really they mostly want to tell someone about all of the fun they are having or the discoveries they are making.

While I enjoy these conversations, and consider them one of the "fringe benefits" of editing MICRO, it disturbs me that many of these enthusiasts who are willing to spend five to ten dollars on a phone call to me, are not willing to spend a little time writing down their

information for publication in MICRO where thousands can share it (and they can earn a few dollars).

MICRO, in order to serve its main purpose of presenting information about all aspects of the 6502 world, needs to receive information from a wide variety of sources. To achieve a more balanced content, we desperately need articles on: industrial, educational, business, home, and other real applications of systems; non-KIM, -Apple, -PET systems, homebrew and commercial; techniques for programming, interfacing, and expanding systems; and many other topics. If you have to your own experience. anything to share, then take the time The "Manuscript to write it down. Cover Sheet" on the next page should serve as a guide and make it a little easier to submit your article.

4:33

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A Few Suggestions

All text material will be retyped. Therefore your format does not matter as long as it is readable. Double spaced, typed, is preferable, but not required. Any figures should be neatly drawn to scale as they will appear in MICRO. If we have to redraw the figures and diagrams, then we normally will pay less for that page. Photographs should be glossy prints either the same size as the final will be or twice the final size. We will re-assemble all programs to obtain clean listings using the syntax we have adopted (see inside back cover - MICRO #1). Since others will be copying your code, please try to thoroughly test it and make sure it is as error free as possible. Submit your articles early. We will try to get a proof back to you for final correction, but with our tight schedule this may not always be possible. Send your manuscripts to:

Robert M. Tripp, Editor, MICRO, P.O. Box 3, So. Chelmsford, MA 01824, U.S.A.

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 - Discusses problems some have encountered in recording data files on tape and reading the information back in. Floto, in his capacity as the PET VET, offers his services on problems met with specific applications of PET.

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 Program to stop data on the video terminal by pressing a key. Handy
 for examining data during a disassembly or a long directory program.
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A program for "justification" of copy to be printed.

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A bare-bones TIM S100 board to use with a terminal such as the CT-64 from SWTP.

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 It is pointed out that the Apple II power supply, although small in physical size, is a switching type which runs cool and is sufficient to run an Apple II with several extra cards plugged into the system.
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 Corrections for Ultratape, MICRO No. 1, p 13; Making Music with the KIM,
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- 291. Dial, William, "6502 Bibliography-Part II", MICRO, No. 3, pp 33-36 (Feb-Mar)
 The second segment of this bibliography covers references 129 to 179 of
 the rapidly growing 6502 literature.
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 No. 3, p 98 (March 1978)
 Adaptable to any 6502 system, this Executive is designed for KIM-1 with 4K or more and TTY or TVT interface. \$25 for listing. From Innovative Software, Inc., 3007 Casa Bonita Dr., Bonita, CA 92002.
- 294. Pollock, James W., "Microprocessors: A Microprocessor controlled CW Keyboard" Ham Radio 11, No. 1, pp 81-87 (Jan. 1978)
 A preprogrammed microcomputer is designed to function as a Morse Code keyboard. Uses a MOS Technology MCS6504 which is a software compatible cousin to the 6502.
- 295. Connecticut Microcomputer, 150 Pocono Rd., Brookfield, CT 06804, New Product Announcement, "RS-232 Adapter for KIM", DDJ 3, No. 21, p 3 (Jan '78)

 The ADApter converts KIM's 20 ma. current loop port to an RS-232 port without affecting the baud rate. \$24.50
- 296. Schick, Paul, "Unsupported OPCODE Pitfalls", DDJ 3, No. 21, p 3 (Jan 1978)
 Comments on the earlier article on 650X Opcodes: DDJ, Aug 1977.
- 297. Moser, Carl, "Memory Test for 6502", DDJ 3, No. 21, pp 4-5, (Jan 1978)

 A program which tests RAM memory in a 6502 based system. I/O is arranged for 6502 TIM based system but can be easily changed.
- 298. Smith, Stephen P., "Challenging Challenger's ROMS", DDJ 3, No. 21, p 6 (Jan) Using the PREK function of the OSI Microsoft BASIC, a disassembler to convert stored bytes in the PROMs or ROMs has been devised.
- 299. Computers One, PO Box 7148, Honolulu, HI 96821, New Product Announcement, On Line 3, No. 2, p 4 (March 1, 1978)
 Pre-recorded programs for PET. "HUSTLERS" includes a number of business oriented programs for checking accounts, rent accounts, legal dairy and trust accounts.
- 300. Lufkin, C.R., 315 Dominion Dr., Newport News, VA 23602, On-Line 3, No 2, p 5 (March 1, 1978)

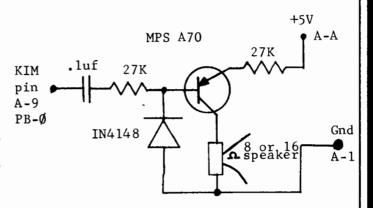
 FITABP is Federal Income Tax Program for PET owners with 8K. Prints out form 1040 Schedule A and B. 11.112

A KIM BEEPER

Gerald C. Jenkins 774 Twin Branch Drive Birmingham, AL 35226

A short blast or two of audio for load errors, end-of-line, etc., is nice to have. This routine requires a simple audio amplifier such as the one in the KIM-1 User Manual, page 57, or the one KIM shown below. Also needed is a latched pin output port, again such as those on the A-9 KIM-1, and a programmable timer.

Enter the routine with the number of blasts in the X register. Change the tone to suit by changing contents of NOTE, \$0114.



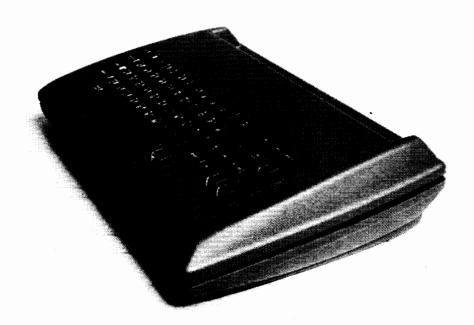
0102 0105	8D A9 8D	07 01 02	17 17	BEEP	STA	TIMER \$01 PBD	START TIMER FOR 1/4 SECOND TONE USING INTERVAL TIMER SET OUTPUT TONE OFF
010D	4D	02	17	TONE	EOR	PBD	TOGGLE OUTPUT
0110					STA		
0113	ΑO	C8			LDYIM	NOTE	SET TO COUNT FOR NOTE LENGTH
0115	88			TONEX	DEY		\$C8 = 500 HZ
0116	DO	FD			BNE	TONEX	CYCLE IN DOWN COUNTER
0118	24	FF			BIT		TEST 1/4 SECOND UP
011A	10	F1			BPL	TONE	CONTINUE TONE IF NOT DONE
011C	A 9	01			LDAIM	\$ 01	TURN TONE OFF
011E	8D	02	-		STA	PBD	
0121	A 9	FF			LDAIM	TIME	START WAIT BETWEEN BEEPS
-							
0126	2C	07	17	NOTONE	BIT	TIMER	WAIT FOR TIME OUT
0129	10	FB			BPL	NOTONE	
012B	CA				DEX		DECREMENT NUMBER OF BEEPS COUNTER
012C		D2				BEEP	ANOTHER BEEP OR
012E	60				RTS		DONE. RETURN TO CALLING ROUTINE

A Few Notes:

- 1. Although the above version is assembled at \$0100, it is relocatable and can be placed anywhere in memory.
- The calling sequence for BEEPER is:

put number of beeps into the X register JSR BEEPER on return A = \$FF, X = \$00, and Y = \$00

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AN APPLE-II PROGRAMMER'S GUIDE

[You Can Get There From Here!]

Rick Auricchio 59 Plymouth Avenue Maplewood, NJ 07040

Most of the power of the APPLE-II comes in a "secret" form - almost undocument-ed software. After several months of coding, experimenting, digging, and writing to APPLE, most of the APPLE's pertinent software details have come to light.

Although most of the ROM software has been printed in the APPLE Reference Manual, its Integer Basic has not been listed; as a result, this article will be limited to Monitor software. Perhaps when a source listing of Integer Basic becomes available, we'll be able to interface with some of its many routines.

First Things First

When I took delivery of my Apple (July 1977), all I had was a "preliminary" manual - no goodies like listings or programming examples. My first letter to Apple brought a listing of the Monitor. Seeing what appeared to be a big jumble of instructions, I set out dividing the listing into logical routines while deciphering their input and output parameters. Once this was done, I could look at portions of the code without becoming dizzy.

The Monitor's code suffers from a few ills:

- 1 Subroutines lack a descriptive "preamble" stating function, calling seqquences, and interface details.
- 2 Many subroutines have several entry points, each of which does something slightly different.
- 3 Useful routines are not documented in a concise form for user access.

I will concede that, while using a "shoehorn" to squeeze as much function as possible into those tiny ROM's, some shortcuts are to be expected. However, those valuable Comment Cards don't use up any memory space in the finished product - 'nuff said.

The Good Stuff

The best way to present the Apple's software interface details is to describe them in tabular form, with further explanation about the more complex ones. The following tables will be found on the back cover of this issue:

Table 1 outlines the important data areas used by the Monitor. These fields are used both internally by the Monitor, and in user communication with many Monitor routines. Not all of the data fields are listed in Table 1.

Table 2 gives a quick description of most of the useful Monitor routines: it contains Name, Location, Function, Input/Output parameters, and Volatile (clobbered) Registers.

Don't hesitate to experiment with these routines - since all the important software is in ROM, you can't clobber anything by trying them out (except what you might have in RAM, so beware).

Using the "User Exits"

The Monitor provides a few nice User Exits for us to get our hands into the Monitor. With these, it is a simple matter to "hook in" special I/O and command-processing routines to extend the Apple's capabilities.

Two of the most useful exits are the KEYIN and COUT exits. These routines, central to the function of the Monitor, are called to read the keyboard and output characters to the screen. By placing the address of a user routine in CSWH/L or KSWH/L, we will get control from the Monitor whenever it attempts to read the keys or output to the screen.

As an example of this exit's action, try this: with no I/O board in I/O Slot 5, key-in "Kc5" (control K, followed by 5, then Return). You'll have to hit Reset to stop the system.

Here's what happened: setting the keyboard to device 5 causes the Monitor to install \$C500 as the "user-exit" address in KSWH/L. This, of course, is the address assigned to I/O Slot 5. Since no board is present, a BRK opcode eventually occurs; the Monitor prints the break and the registers, then reads for another command. Since we still exit to \$C500, the process repeats itself endlessly. Reset removes both user exits; you must "re-hook" them after every Reset.

These two exits can enable user editing of keyboard input, printer driver programs, and many other ideas. Their use is limited to your ingenuity.

Another useful exit is the Control Y command exit. Upon recognition of Control Y, the Monitor issues a JSR to location \$03F8. Here the user can process commands by scanning the original typed line or reading another. This exit is often very useful as a shorthand method of running a program. For example, when you're going back and forth between the Monitor and the Mini-Assembler, typing "F666G" is a bit tiresome. By placing a JMP \$F666 in location \$03F8, you can enter the Mini-Assembler via a simple Control Y.

Upon being entered from the Monitor at \$03F8, the registers are garbage. Locations A1 and A2 contain converted values from the command (if any), and an RTS gets you neatly back into the Monitor. Figure 1 shows this in more detail

Figure 1: Control Y Interface

Command typed:

*1234.F5A7Yc

Upon entry at \$03F8, the following exists:

A1L (\$3C) contains \$34 A1H (\$3D) contains \$12 A2L (\$3E) contains \$A7 A2H (\$3F) contains \$F5

Hardware Features

One of the best hardware facilities of the Apple-II, the screen display, is also the "darkest" - somewhat unknown. Here's what I've found out about it.

The screen buffer resides in memory pages 4 through 7, locations \$0400 through about \$07F8. The Secondary screen page, although not accessed by the Monitor, occupies locations \$0800 through \$0BF8. Screen lines are not in sequential memory order; rather, they are addressed by a somewhat complex calculation carried out in the routine BASCALC. What BASCALC does is to compute the base address for a particular line and save it; whenever the cursor's vertical position changes, BASCALC recomputes the base address. Characters are stored into the screen buffer by adding the base address to the cursor's horizontal position.

I haven't made too much use of directly storing characters into the screen buffer; usually just storing new cursor coordinates will do the trick via the Monitor routines. Be careful, though - only change vertical position via the VTAB routine since the base address must get recomputed!

Characters themselves are internally stored in 6-bit format in the screen buffer. Bit 7 (\$80), when set, forces normal (white-on-black) video display for the character. If Bit 7 is reset, the character appears inverse (black-on-white) video. Bit 6 (\$40), when set, enables blinking for the character; this occurs only if Bit 7 is off. Thus an ASCII "A" in normal mode is \$81; in inverse mode, \$01; in blinking mode, \$41.

Reading the keyboard via location \$C000 is easy; if Bit 7 (\$80) is set, a key has been pressed. Bits 0 - 6 are the ASCII keycode. In order to enable the keyboard again, its strobe must be cleared by accessing location \$C010. Since the keyboard is directly accessible, there is no reason you can't do "special" things in a user program based on some keyboard input - if you get keys directly from the keyboard, you can bypass ALL of the Control and Escape functions.

AN APPLE II PROGRAMMER'S GUIDE

Rick Auricchio 59 Plymouth Avenue Maplewood, NJ 07040

MONITOR Data Areas in Page Zero

Name	Loc.	Function
WNDLEFT	20	Scrolling window: left side (0-\$27)
WNDWDTH	21	Scrolling window: width (1-\$28)
WNDTOP	22	Scrolling window: top line (0-\$16)
WNDBTM	23	Scrolling window: bottom line (1-\$17)
CH	24	Cursor: horizontal position (0-\$27)
CV	25	Cursor: vertical position (0-\$17)
COLOR	30	Current COLOR for PLOT/HLIN/VLIN functions
INVFLG	32	Video Format Control Mask:
		\$FF=Normal, \$7F=Blinking, \$3F=Inverse
PROMPT	33	Prompt character: printed on GETLN CALL
CSWL	36	Low PC for user exit on COUT routine
CSWH	37	High PC for user exit on COUT routine
KSWL	38	Low PC for user exit on KEYIN routine
KSWH	39	High PC for user exit on KEYIN routine
PCL	3 A	Low User PC saved here on BRK to Monitor
PCH	3B	High User PC saved here on BRK to Monitor
A1L	3C	A1 to A5 are pairs of Monitor work bytes
A1H	3D	
AZL	3E	
A2H	3F	
A3L	40	
A3H	41	
A4L	42	
A4H	43	
A5L	44	
A5H	45	and the second s
ACC	45	User AC saved here on BRK to Monitor
XREG	46	User X saved here on BRK to Monitor
YREG	47	User Y saved here on BRK to Monitor
STATUS	48	User P status saved here on BRK to Monitor
SPNT	49	User Stack Pointer saved here on BRK

Page 2 (\$0200-\$02FF) is used as the KEYIN Buffer.

Pages 4-7 (\$0400-\$07FF) are used as the Screen Buffer. Page 8 (\$0800-\$08FF) is the "secondary" Screen Buffer.

Table 1.

AM APPLE II PROGRAMMER'S GUIDE

MONITOR ROUTINES

Name	Loc.	Steps On	Function
PLOT	F800	AC	Plot a point. COLOR contains color in both halves of byte (\$00-\$FF). AC: y-coord, Y: x-coord.
CLRSCR	F832	AC,Y	Clear screen - graphics mode.
SCRN	F871	AC	Get screen color. AC: y-coord, Y: x-coord.
INSTDSP	F8D0	ALL	Disassemble instruction at PCH/PCL.
PRNTYX	F940	AC	Print contents of Y and X as 4 hex digits.
PRBL2	F94C	AC, X	Print blanks: X is number to print.
PREAD	FB1E	AC,Y	Read paddle. X: paddle number 0-3.
SETTXT	FB39	AC	Set TEXT mode.
SETGR	FB40	AC	Set GRAPHIC mode (GR).
VTAB	FC22	AC	VTAB to row in AC (0-\$17).
CLREOP	FC42	AC,Y	Clear to end-of-page.
HOME	FC58	AC,Y	Home cursor and clear screen.
SCROLL	FC70	AC, Y	Scroll up one line.
CLREOL	FC9C	AC,Y	Clear to end-of-line.
NXTA4	FCB4	AC	Increment A4 (16 bits), then do NXTA1.
NXTA1	FCBA	AC	Increment A1 (16 bits). Set carry if result >= A2.
RDKEY	FDOC	AC,Y	Get a key from the keyboard.
RDCHAR	FD35	AC,Y	Get a key, also handles ESCAPE functions.
GETLN	FD6A	ALL	Get a line of text from the keyboard, up to the carriage return. Normal mode for Monitor. X returned with number of characters typed in.
CROUT	FD8E	AC.Y	Print a carriage return.
PRBYTE	FDDA	AC	Print contents of AC as 2 hex digits.
COUT	FDED	AC,Y	Print character in AC; also works for CR, BS, etc.
PRERR	FF2D	AC.Y	Print "ERR" and bell.
BELL	FF3A	AC, Y	Print bell.
RESET	FF59		RESET entry to Monitor - initialize.
MON	FF65		Normal entry to 'top' of Monitor when running.
SWEET16	F689	None	SWEET16 is a 16-bit machine language interpreter.
			[See: SWEET16: The 6502 Dream Machine, Steve Wozniak,] [BYTE, Vol. 2, No. 11, November 1977, pages 150-159.]

Table 2.